

IFWsrch.txt

<http://www.google.com/>

"The Virtual Test program (VTest)"

"Dynamic Test Emulation for EDA-Based Mixed-Signal Test Development Automation"

"Emulation-based Design Errors Identification"

"Sunburst Design - Introduction to Verilog-2001 Design"

"Introduction to Verilog-2001 Design"

www.sunburst-design.com/verilog_training/intro_verilog_training.pdf

"Manual and Automatic VHDL/Verilog Test Bench Coding Techniques"